



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/699,221 Confirmation No.: 2799
First Named Inventor: Constantin Bulucea Filing Date: 31 October 2003
Group Art Unit: 2814 Examiner: Unknown
Atty. Docket No.: NS-5127-1D US
Title: Gate-Enhanced Junction Varactor With Gradual Capacitance Variation
Assignee: National Semiconductor Corporation

Mountain View, California
9 May 2005

COMMISSIONER FOR PATENTS
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Alexandria, Virginia 22313-1450

PRELIMINARY AMENDMENT

Sir:

Please amend the above patent application in the following manner.

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01 FC:1202 2400.00 DA
02 FC:1201 600.00 DA

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IN THE TITLE

Amend the title as follows: DESIGN AND OPERATION OF GATE-ENHANCED
JUNCTION VARACTOR WITH GRADUAL CAPACITANCE VARIATION

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